







<section-header> Dutline • Private Set Intersection (PSI) • Why does size matter? • Size-Hiding via other techniques? • Size-Hiding even possible? • SHI-PSI: Size-Hiding PSI • Security of SHI-PSI • Ite cost of Size-Hiding • The cost of Size-Hiding • Conclusion





















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SHI-	SHI-PSI: Notation							
	Symbol	Meaning						
	$\lambda,\lambda_1,\lambda_2$	Security Parameters						
	p,q	Safe primes						
	N=pq	Safe RSA modulus						
	g	Generator of QR _N						
	Н()	Random Oracle <i>H</i> : $\{0,1\}^* -> \{0,1\}^{\lambda_1}$						
	F()	Random Oracle <i>F:</i> $\{0,1\}^* -> \{0,1\}^{\lambda_2}$						
	C,S	Client and Server sets						
	<i>V, W</i>	Sizes of C and S						
	i∈[1, <i>v</i>]	Index of elements of C						
	j∈[1,w]	Index of elements of S						
	C _i , S _j	Generic elements of C and S						
	hci, hsj	$H(c_i)$, $H(s_j)$						
	π	Random permutation						











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The Cost of Hiding Size: PSIs vs SHI-PSI									
	Tools	Model	Adv	Server Op	Client Op	Bandwidth			
[FNP04]	Oblivious Poly Eval	Standard/ ROM	HbC/ Malicious	O(wloglog(v)) 160-bit mod1024 exps	O(w+v) 160-bit mod 1024 exps	O(w+v)			
[KS05]	Oblivious Poly Eval	Standard	HbC Malicious*	O(w·v) <i>m</i> -bit mod 2048 exps	O(w+v) <i>m</i> -bit mod 2048 exps	O(w+v)			
[JL09]	OPRF q-DDH	Standard CRS	Malicious	O(w) <i>m</i> -bit mod 2048 exps	O(v) <i>m</i> -bit mod 2048 exps	O(w+v)			
[HN10]	DDH	Standard	Malicious	O(wloglog(v)) 160 mod 1024-bit exps	O(w+v) 160-bit mod 1024 exps	O(w+v)			
[JL10]	OneMore- DH	ROM	Malicious	O(w+v) 160-bit mod 1024 exps	O(v) 160-bit mod 1024 exps	O(w+v)			
[DT10]	OneMore- RSA	ROM	HbC	O(w+v) 1024-bit mod 1024 exps	O(v) mod <u>mults</u>	O(w+v)			
[DKT10]	DDH	ROM	Malicious	O(w+v) 160-bit mod 1024 exps	O(v) 160-bit mod 1024 exps	O(w+v)			
SHIPSI	RSA	ROM	<u>HbC</u>	O(w) 1024-bit mod 1024 exps	O(vlog(v)) 1024-bit mod 1024 exps	O(w)			
v = C $w = S $									







































